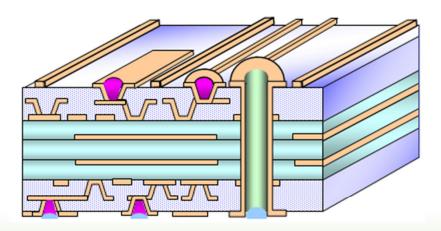


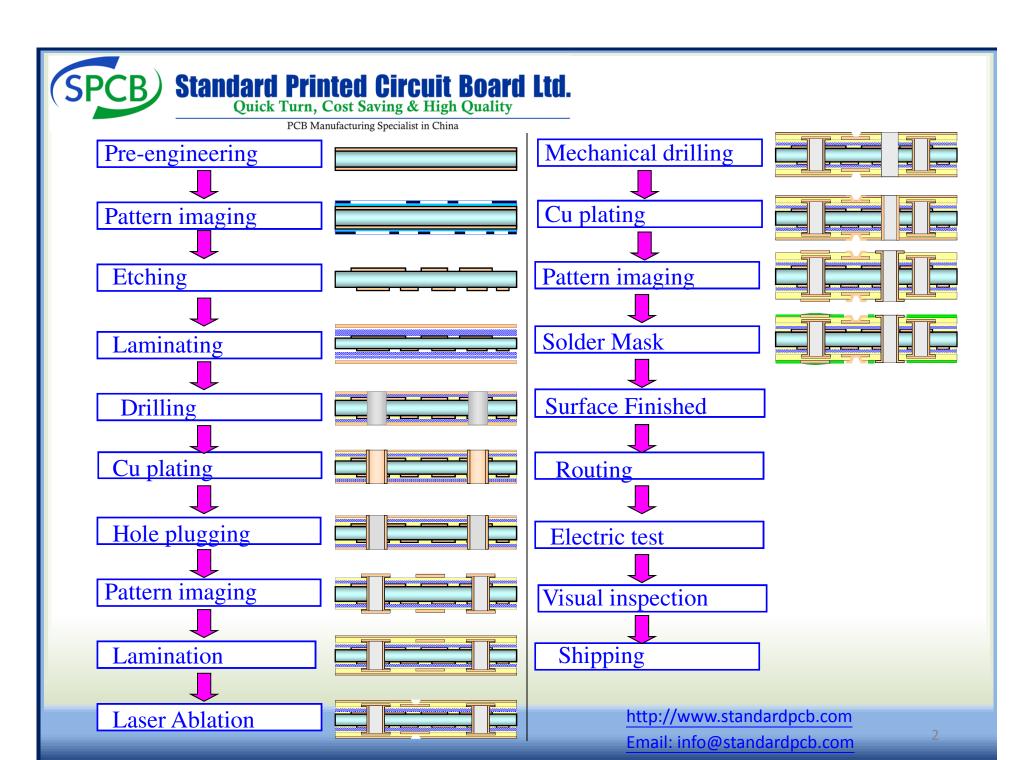
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HDI Process Flow





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Pre-engineering



Pattern imaging



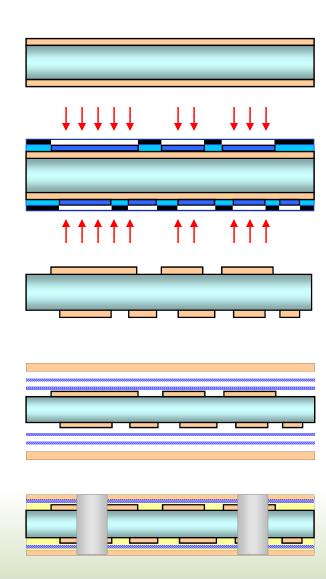
Etching



Laminating



Drilling

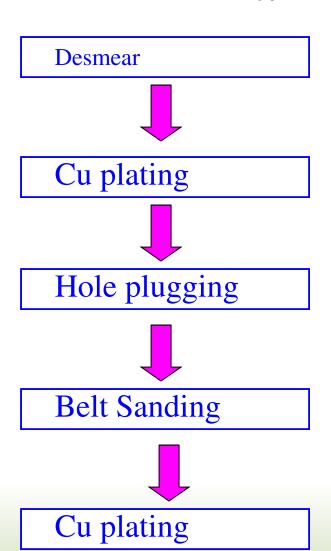


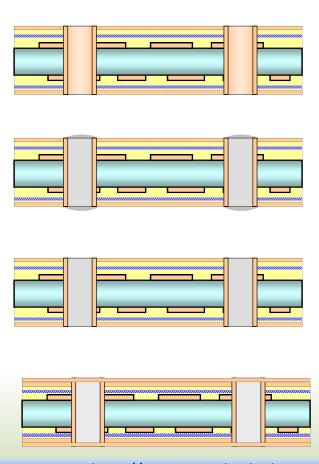
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Pattern imaging



Lamination



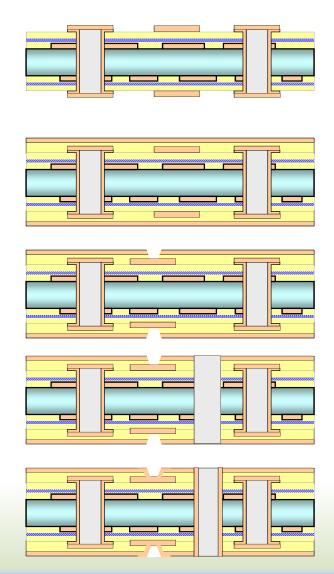
Laser Ablation



Mechanical drilling



Cu plating



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Pattern imaging



Solder Mask



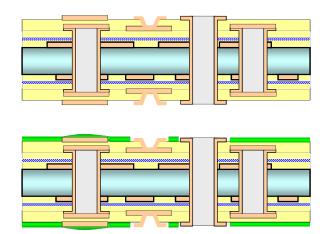
Gold plating



Routing



Electrical test





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Visual inspection



Hole counter



Shipping

* Raw material (Thin Core, Copper, Prepreg.....)

Raw Material : FR-4 (Difuntional, Tetrafuntional)

Supplier : EMC ,Nan-Ya

Sheet size : 36"*48", 40"*48", 42"*48

Core Thickness: 0.003", 0.004", 0.005", 0.006"

0.008", 0.010", 0.012", 0.015"

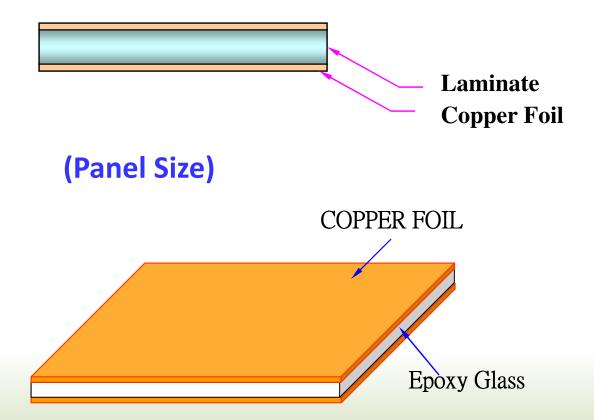
0.021", 0.031", 0.039", 0.047"

Copper Foil : 1/3 oz,1/2 oz,1.0 oz,2 oz

Prepreg type : 1080,2113,2116,1506,7628,7630



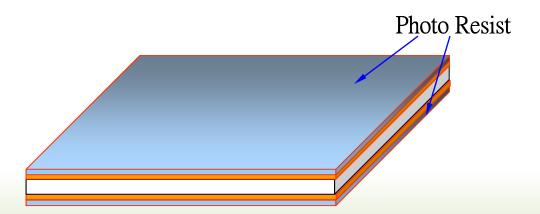
1. Innerlayer (THIN CORE)





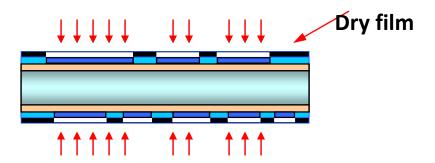
2. Dry Film Resist Coat

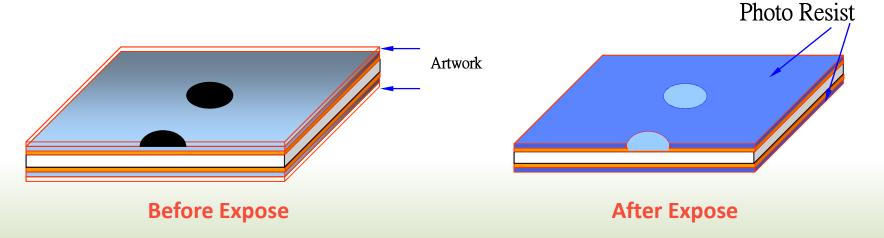






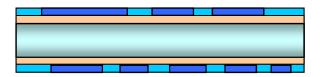
3. Inner Artwork Expose

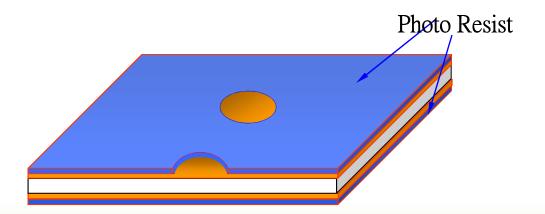






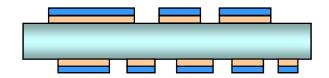
4. Inner layer image Develop

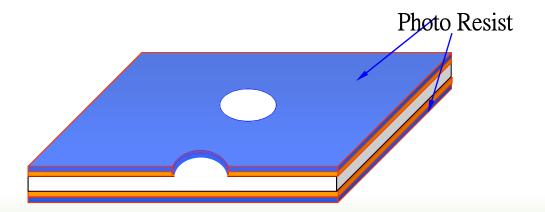






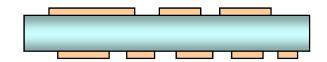
5. Inner Layer Etch

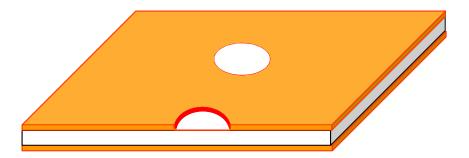






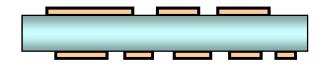
6. Inner Layer Strip Resist

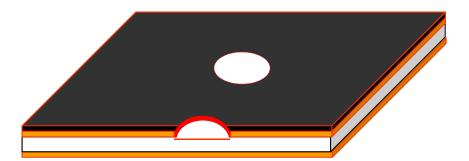




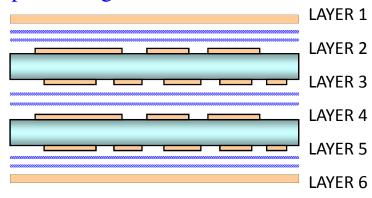


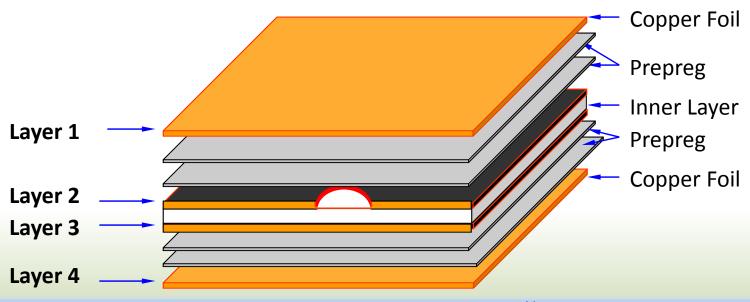
7. Oxide Coating





8. Lay-up Building

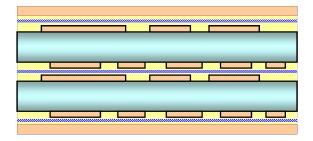


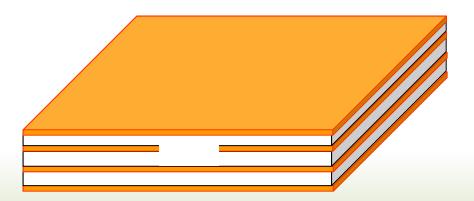




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9. Lamination





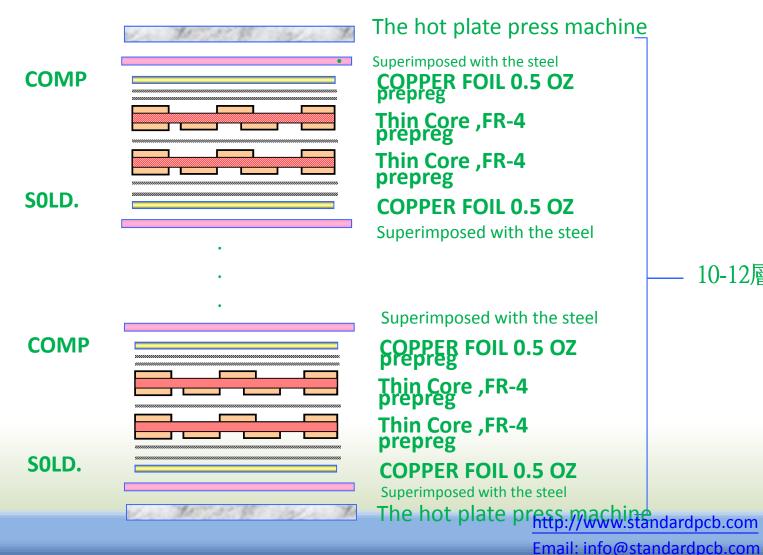


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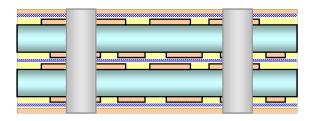
Typical of the multilayer structure of stacked plates and laminated

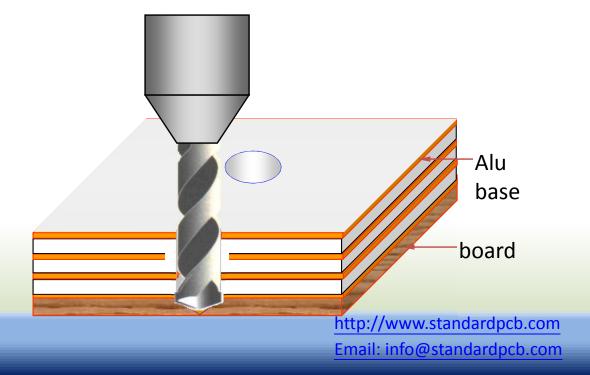


10-12層疊合



10. NC Drilling

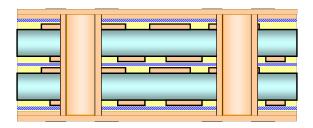


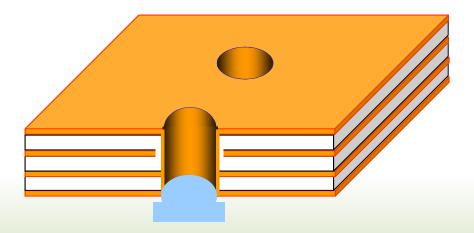




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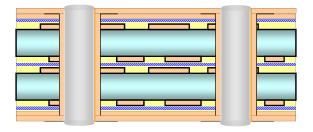
11. Desmear & Copper Deposition



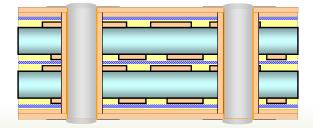




12. Hole Plugging



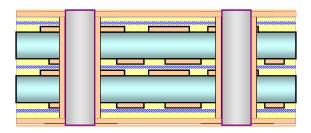
13. Belt Sanding



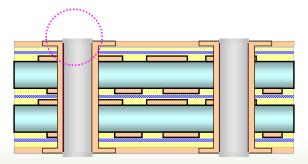


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14. Copper Reduction \rightarrow Option

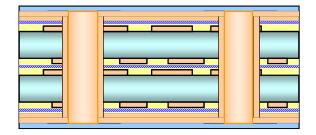


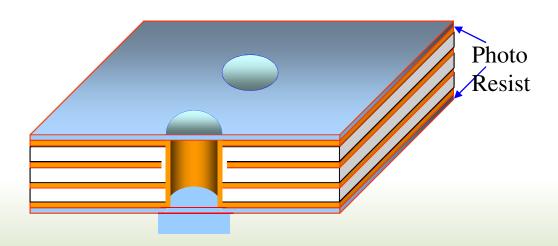
15. Belt Sanding \rightarrow Option





16. Dry Film Lamination (Outer layer)

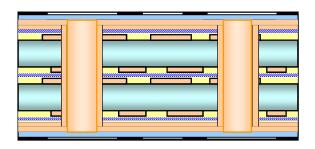


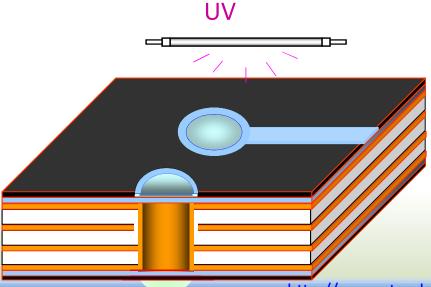




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17. Outerlayer Expose





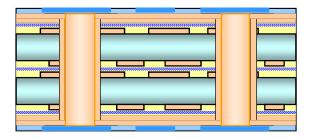
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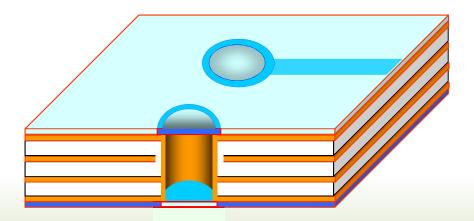
Email: info@standardpcb.com



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18. After Exposed

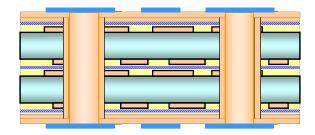


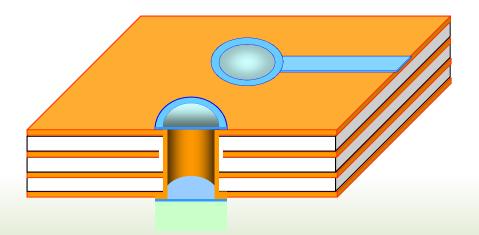




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19. Outerlayer Develop

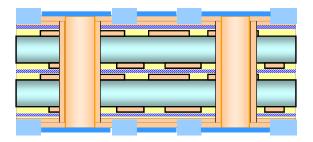


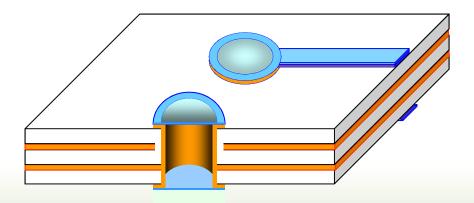




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20. Etch

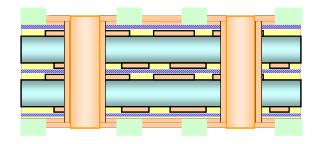


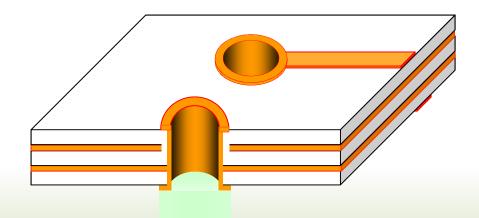




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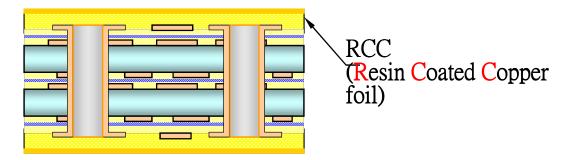
20. Strip Resist

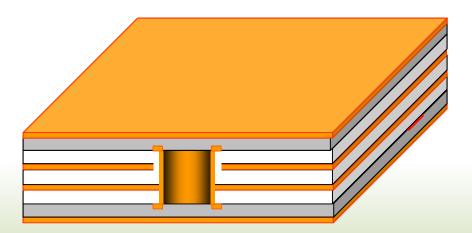






21. Build-up Layer Lamination







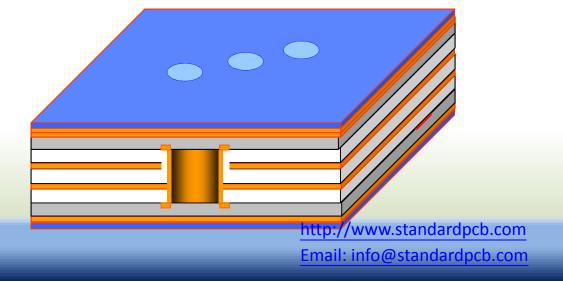
22. Conformal Mask

Before Exposure

Artwork

Artwork

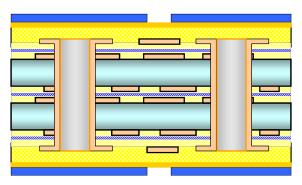
After Exposure

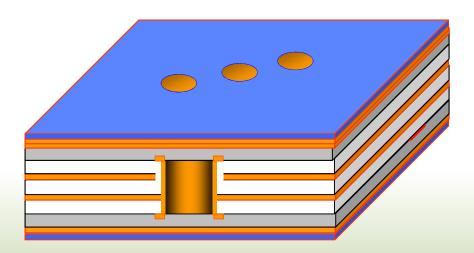




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23.Conformal Mask

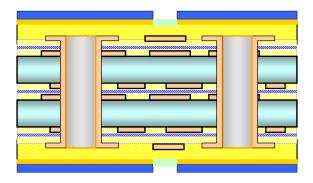


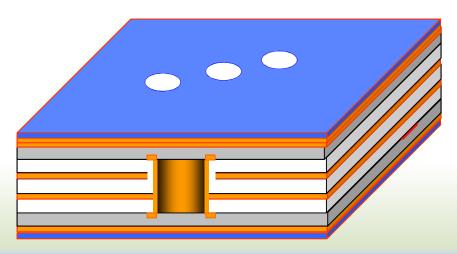




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24. Conformal Mask (for Etching)

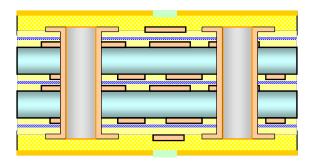


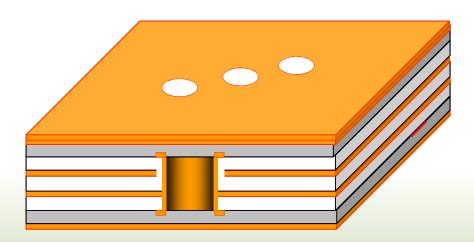




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25. Conformal Mask

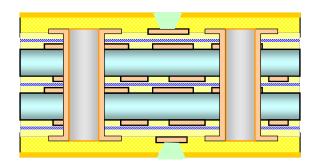


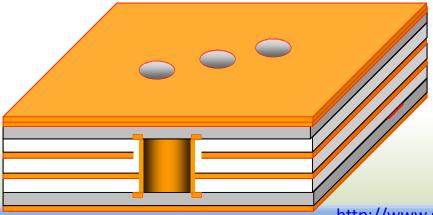




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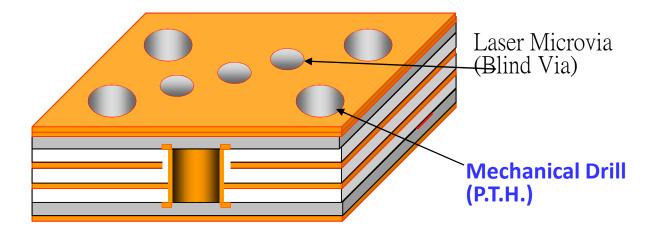
26. Laser Ablation and NC Drilling







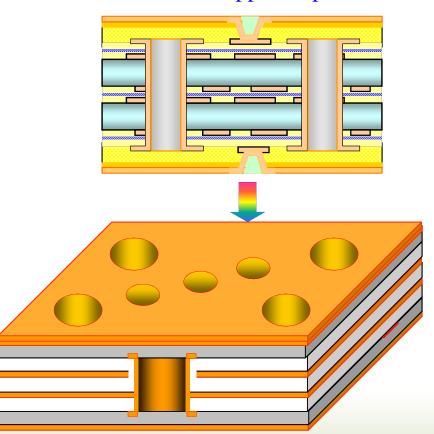
27. Mechanical Drill





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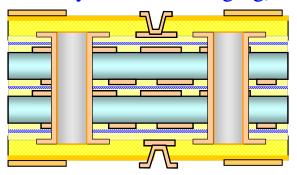
28. Desmear & Copper Deposition



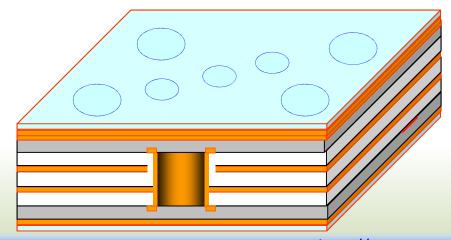


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29. Outerlayer Pattern imaging)



Dry Film Lamination)

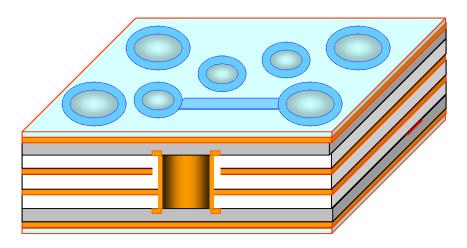


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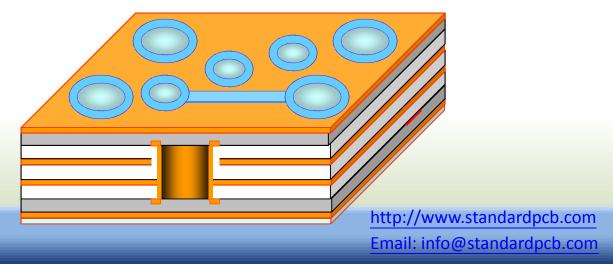


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Outerlayer Exposure



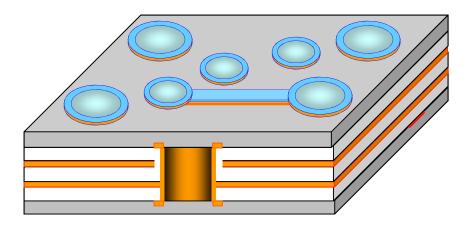
Dry Film Developing



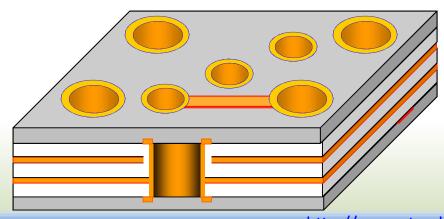


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Etching



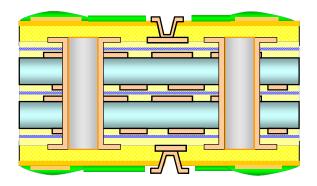
Dry Film Stripping

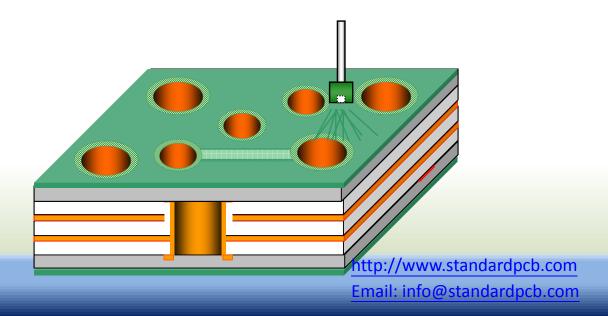




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30. Solder Mask Process

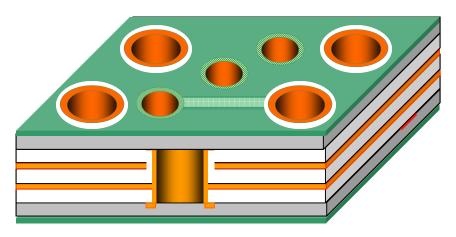




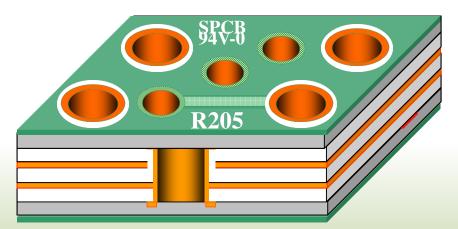


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31. S/M Developing



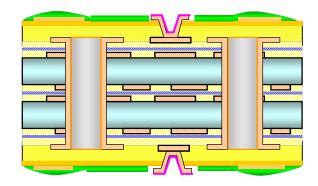
32. Legend Printing

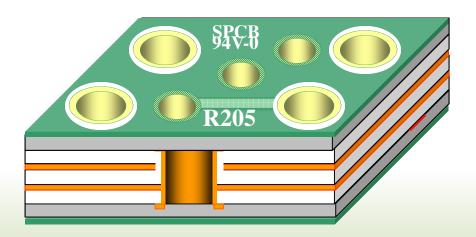




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33. Electroless Ni/Au , HAL_lead free and Etc.

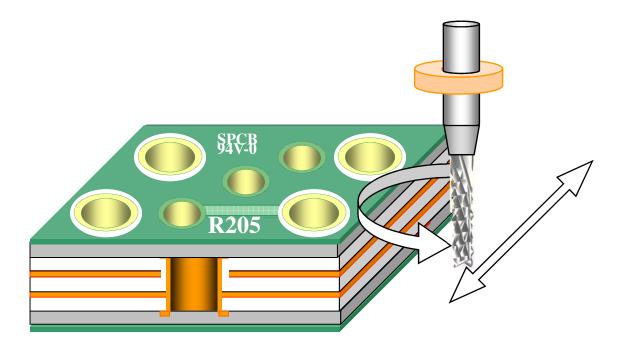






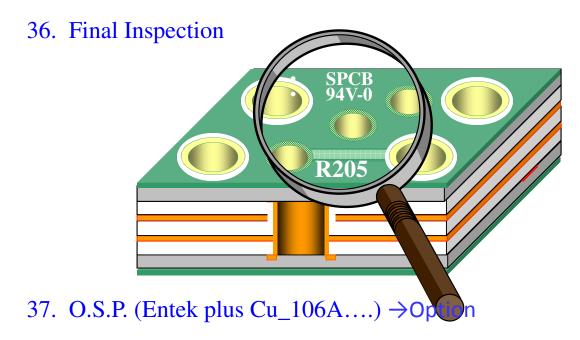
PCB Manufacturing Specialist in China

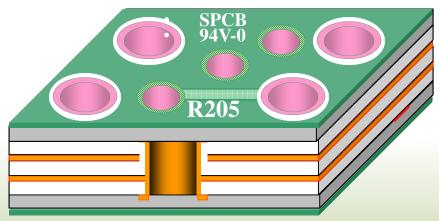
34. Outline Profile and Electrical Testing





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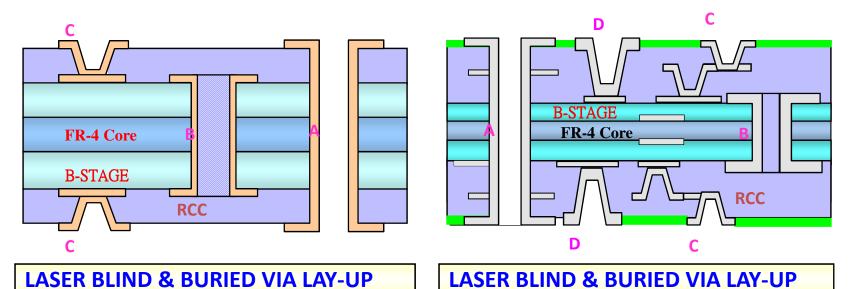
BURIED VIA AND LASER BLIND VIA OPTION

A = THROUGH VIA HOLE

B = BURIED VIA HOLE

C = One Level Laser Blind Via

D = Two Level Laser Via





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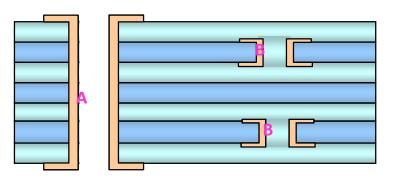
BLIND AND BURIED VIA OPTION

A = THROUGH VIA HOLE

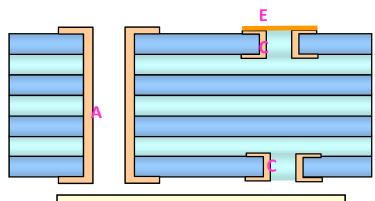
B = BURIED VIA HOLE

C = BLIND VIA HOLE

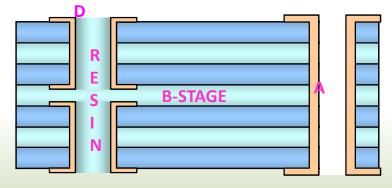
D = BLIND HOLE MLB VIA







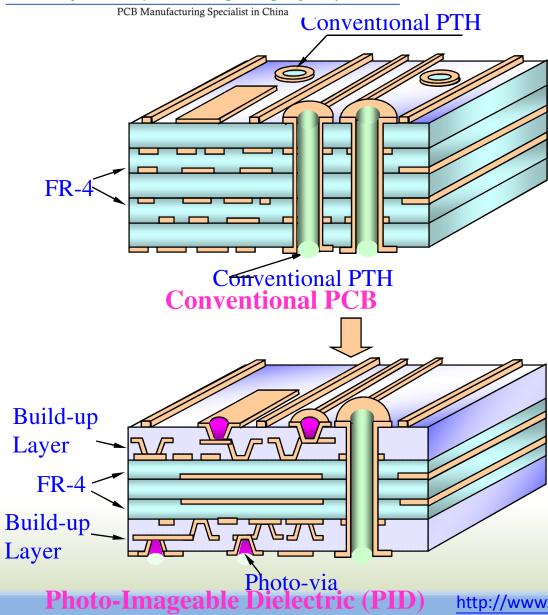
BLIND VIA LAY-UP



BLIND VIA SEQUENTIAL LAY-URttp://www.standardpcb.com

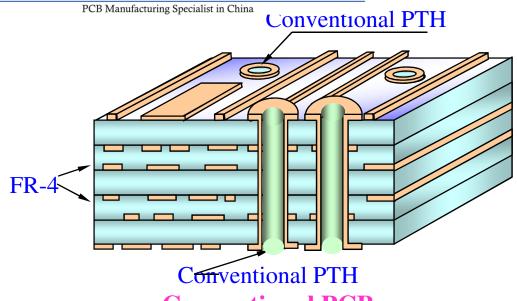
Email: info@standardpcb.com



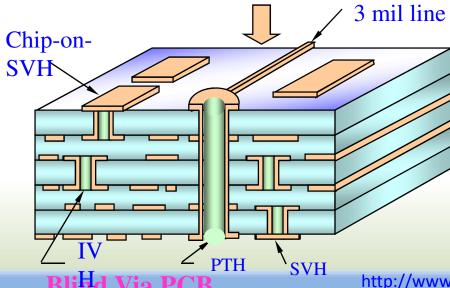


http://www.standardpcb.com Email: info@standardpcb.com





Conventional PCB



http://www.standardpcb.com

Email: info@standardpcb.com

End



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